


**REMARKS**

If there are any additional charges, please charge Deposit Account No. 02-2666. If a telephone interview would in any way expedite the prosecution of this application, the Examiner is invited to contact Robert B. O'Rourke at (408) 720-8300.

Respectfully submitted,

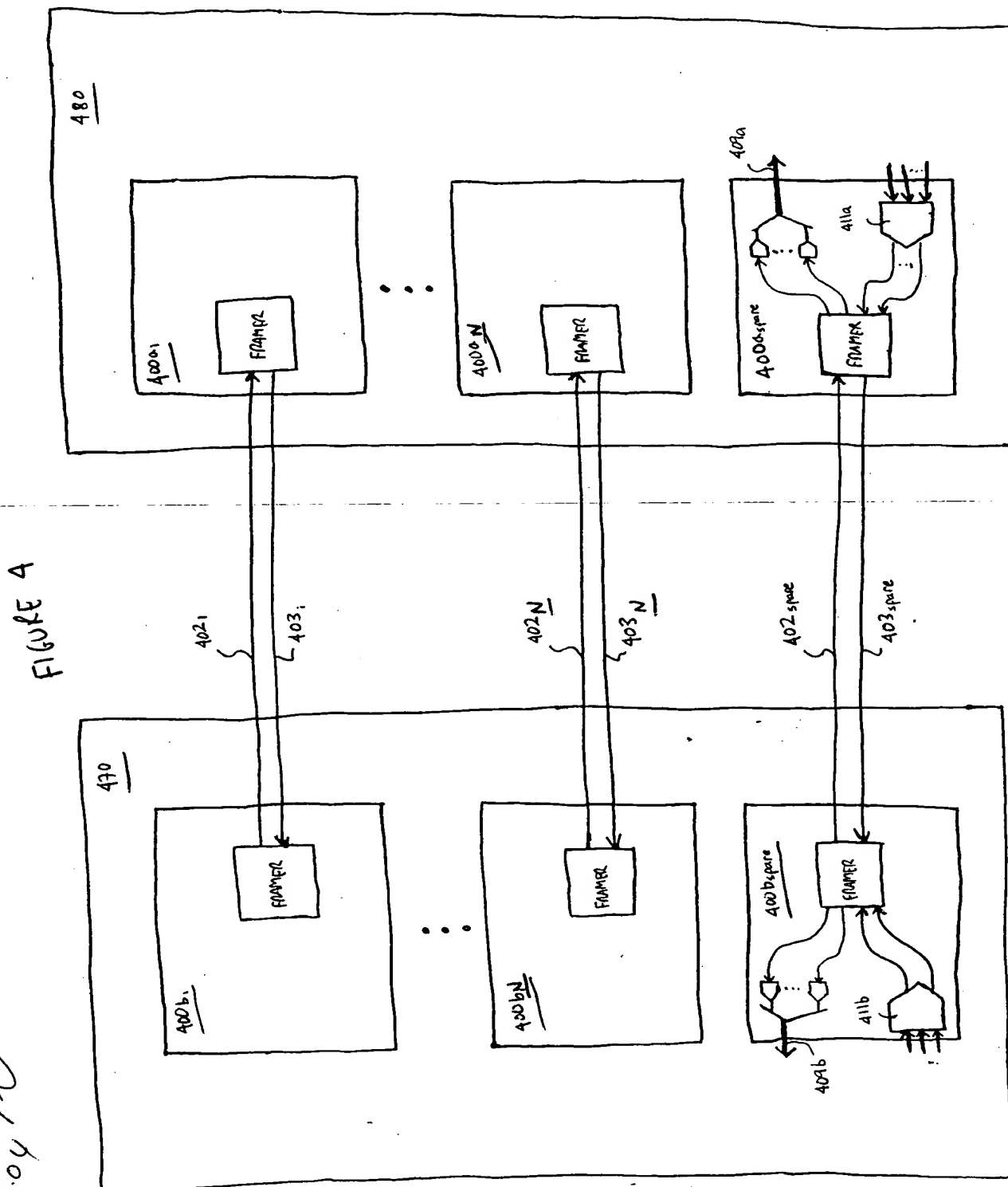
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/4/04

  
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Robert B. O'Rourke  
Reg. No. 46,972

12400 Wilshire Blvd.  
Seventh Floor  
Los Angeles, CA 90025-1030  
(408) 720-8300

Figure 3 is a block diagram illustrating a system architecture. The system is divided into two main sections, 370 and 380, separated by a dashed line. Each section contains two main processing blocks, 300a and 300b in section 370, and 300c and 300d in section 380. Each of these blocks contains a 'FRAMER' component. The FRAMER in 300a is connected to a '304a' block, which is further connected to a '305a' block. The FRAMER in 300b is connected to a '304b' block, which is further connected to a '305b' block. The FRAMER in 300c is connected to a '304c' block, which is further connected to a '305c' block. The FRAMER in 300d is connected to a '304d' block, which is further connected to a '305d' block. The system is connected via a central bus structure, 302a and 302b, which links the FRAMER components across the sections. Various other components are labeled, including 308a, 308b, 308c, 308d, 309a, 309b, 309c, 309d, 310a, 310b, 310c, 310d, 311a, 311b, 311c, 311d, 312a, 312b, 312c, 312d, and 312x. The diagram shows a complex interconnection of these components, suggesting a multi-processor or multi-channel system architecture.



Approved PR  
6.16.04